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Zhang et al.
 APPLICANT

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 GROUP

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Anh Q. Tran
 EXAMINER

**INFORMATION DISCLOSURE
 STATEMENT BY APPLICANT**



U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA						
AB						
AC						
AD						
AE						
AF						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
AT	AG 0766396	04/02/1997	EP			YES
AH						
AI						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL		
AT	AJ	"Voltage-Tolerant Monolithic L-Band GaAs SPDT Switch," Cooper et al., <i>IEEE MTT-S Digest</i> (1989).
AT	AK	"A Dual-Gate Shorted-Anode Silicon-on-Insulator Lateral Insulated Gate Bipolar Transistor with Floating Ohmic Contact for Suppressing Snapback and Fast Switching Characteristics," Oh et al., <i>Microelectronics Journal</i> 30 pp. 577-581 (1999).
	AL	
	AM	
	AN	
	AO	

EXAMINER	<i>Anh Q. Tran</i>	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.